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Analog-to-Digital and Digital-to-Analog Converters describes in depth converter specifications like Effective Number of Bits (ENOB), Spurious Free Dynamic Range (SFDR), Integral Non-Linearity (INL), Differential Non-Linearity (DNL) and sampling clock jitter requirements. Relations between these

specifications and practical issues like matching of components and offset parameters of differential pairs are derived. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes the requirements of input and signal reconstruction filtering in case a converter is applied into

a signal processing system. CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters describes design details of high-speed A/D and D/A converters, high-resolution A/D and D/A converters, sample-and-hold amplifiers, voltage and current references, noise-shaping converters and sigma-delta

converters, technology parameters and matching performance, comparators and limitations of comparators and finally testing of converters.

This book presents state-of-the-art techniques for

radiation hardened high-resolution Time-to-Digital converters and low noise frequency synthesizers. Throughout the book, advanced degradation mechanisms and error sources are discussed and several ways to prevent such errors are presented. An overview of the prerequisite physics of nuclear interactions is

given that has been compiled in an easy to understand chapter. The book is structured in a way that different hardening techniques and solutions are supported by theory and experimental data with their various tradeoffs. Based on leading-edge research, conducted in collaboration between KU Leuven and CERN,

the European Center for Nuclear Research Describes in detail advanced techniques to harden circuits against ionizing radiation Provides a practical way to learn and understand radiation effects in time-based circuits Includes an introduction to the underlying physics, circuit design, and advanced techniques

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accompanied with experimental data

CMOS short for complementary metal oxide semiconductor is widely used for designing high performance, low power integrated circuits for numerous applications. Basics of CMOS Cell Design introduces the design and simulation of CMOS

integrated circuits in deep sub-micron technology. The book covers the MOS device, inverters, logic gates, arithmetics, interconnects and analog basic cells. A second book includes an extensive presentation of analog cells, radio-frequency analog blocks, analog to digital to analog converter principles,

input/output interfacing silicon-insulator technology, and a discussion on future developments in microelectronics. The CD accompanying this book includes the lite 3 version of the PC tools MICROWIND and DSCH.

The development of large-scale integrated systems

on a chip has had a dramatic effect on circuit design methodology. Recent years have seen an escalation of interest in systems level integration (system-on-a-chip) and the development of low power, high chip density circuits and systems. Kurt Hoffmann sets out to address a wide range of issues relating to the design and integration of

integrated circuit components and provides readers with the methodology by which simple equations for the estimation of transistor geometries and circuit behaviour can be deduced. The broad coverage of this unique book ranges from field effect transistor design, MOS transistor modelling and the

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fundamentals of digital CMOS circuit design through to MOS memory architecture and design. Highlights the increasing requirement for information on system-on-a-chip design and integration. Combines coverage of semiconductor physics, digital VLSI design and analog integrated circuits in one volume for the

first time. Written with the aim of bridging the gap between semiconductor device physics and practical circuit design. Introduces the basic behaviour of semiconductor components for ICs and covers the design of both digital and analog circuits in CMOS and BiCMOS technologies. Broad coverage will appeal to

both students and practising engineers alike. Written by a respected expert in the field with a proven track record of publications in this field. Drawing upon considerable experience within both industry and academia, Hoffmann's outstanding text, will prove an invaluable resource for designers, practising engineers in

the semiconductor device field and electronics systems industry as well as Postgraduate students of microelectronics, electrical and computer engineering.

CMOS chips are becoming increasingly important in computer circuitry. They have been widely used during the

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past decade, and they will continue to grow in popularity in those application areas that demand high performance.

Challenging the prevailing opinion that circuit simulation can reveal all problems in CMOS circuits, Masakazu Shoji maintains that simulation cannot completely

remove the often costly errors that occur in circuit design. To address the failure modes of these circuits more fully, he presents a new approach to CMOS circuit design based on his systematizing of circuit design error and his unique theory of CMOS digital circuit operation. In analyzing CMOS digital circuits, the author

focuses not on effects originating from the characteristics of the device (MOSFET) but on those arising from their connection. This emphasis allows him to formulate a powerful but ultimately simple theory explaining the effects of connectivity by using a concept of the states of the circuits, called microstates. Shoji

introduces microstate sequence diagrams that describe the state changes (or the circuit connectivity changes), and he uses his microstate theory to analyze many of the conventional CMOS digital circuits. These analyses are practically all in closed-form, and they provide easy physical interpretation of the

circuit's working mechanisms, the parametric dependence of performance, and the circuit's failure modes. Originally published in 1992. The Princeton Legacy Library uses the latest print-on-demand technology to again make available previously out-of-print books from the distinguished backlist of Princeton University

Press. These editions preserve the original texts of these important books while presenting them in durable paperback and hardcover editions. The goal of the Princeton Legacy Library is to vastly increase access to the rich scholarly heritage found in the thousands of books published by Princeton University Press since its founding

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as

addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the sigificant changes that must be taken into

account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects,

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dynamic circuits,
memory circuits,
arithmetic building
blocks, clock and I/O
circuits, low power
design techniques, design
for manufacturability and
design for testability.

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